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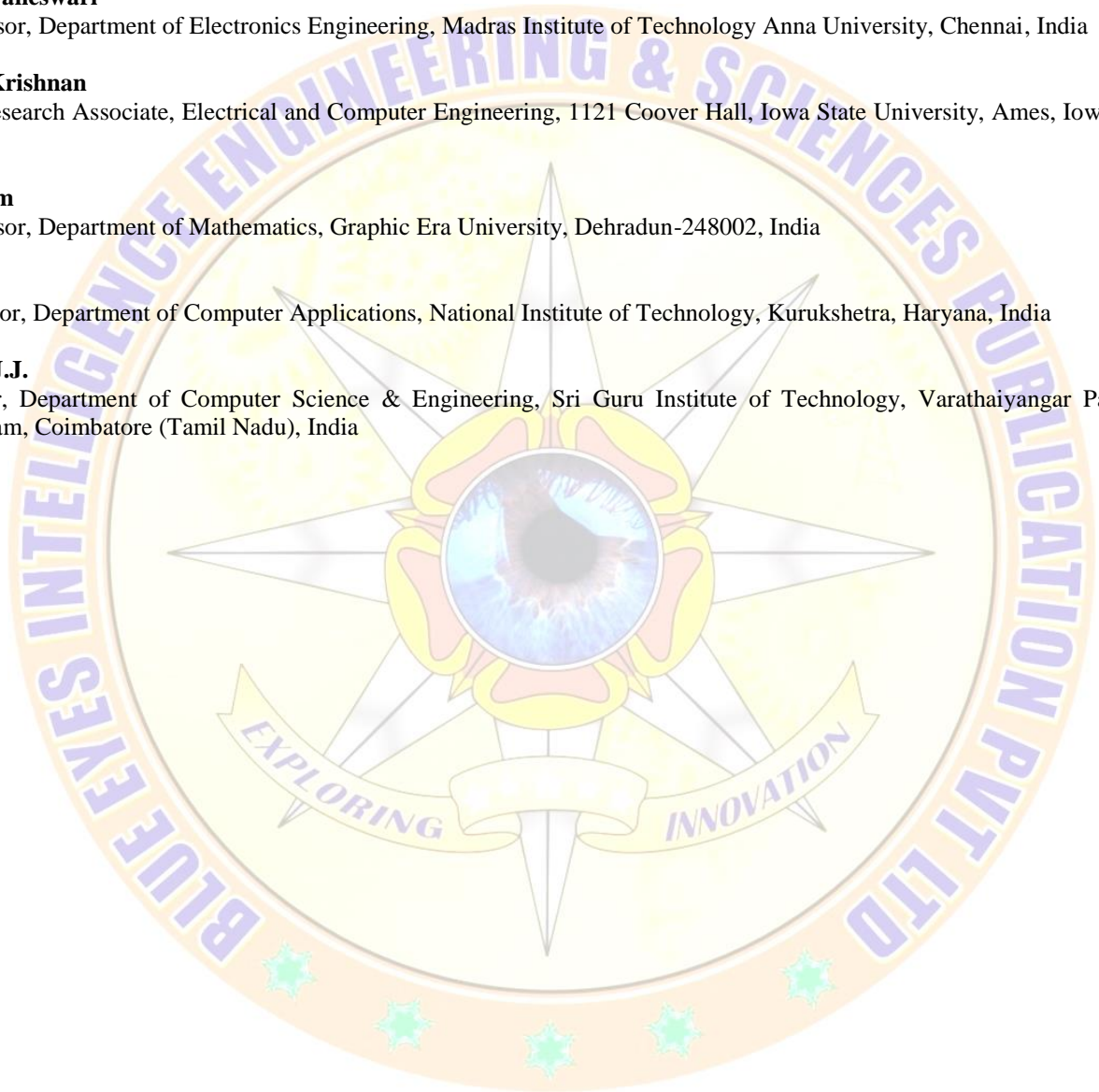
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1.	<b>Authors:</b>	<b>Amrik Singh, Yoginder Talwar, Ajay Prasad</b>	
	<b>Paper Title:</b>	<b>Highly Secure and Fast AES Algorithm Implementation on FPGA with 256 bit key size</b>	
	<p><b>Abstract:</b> The Block cipher AES is a symmetric key cryptographic standard used for transferring block of data in secure manner for server based communication networks, SCADA systems for Oil refinery, Oil and Gas Pipe Lines, and Smart Grids based applications. High level security of data transfer needs long key size i.e. 256 bits, analysis of certain ideas of round key expansion mechanisms from given key data are discussed and the same is implemented in FPGA configuration with 128 bits and 256 bits key size to achieve low latency, high throughput with high security.</p> <p><b>Keywords:</b> Advance Encryption Standard, encryption, decryption, FPGA, VHDL, Virtex-5.</p> <p><b>References:</b></p> <ol style="list-style-type: none"> <li>1. J. Daemen and V. Rijmen. AES proposal: Rijndael. In AES Round 1 Technical Evaluation, NIST 1998. (see: <a href="http://www.esat.kuleven.ac.be/rijmen/rijndael/">http://www.esat.kuleven.ac.be/rijmen/rijndael/</a>, <a href="http://www.nist.gov/aes">http://www.nist.gov/aes</a>)</li> <li>2. N ferguson, R. Schroeppel, D. Whiting. A simple algebraic representation of Rijndael Selected Area in Cryptography, SAC 2001, LNCS 2259, Springer-Verlag, 2001, pp.103-111.</li> <li>3. Courtois, N.T. and J. Pieprzyk: Cryptanalysis of Block Ciphers with over defined Systems of Equations. Accepted by, Asiacrypt 2002, Dec 2002. (See: <a href="http://eprint.iacr.org/2002/044">http://eprint.iacr.org/2002/044</a>).</li> <li>4. Y. Talwar, C.E. Veni Madhavan, N. Rajpal, "On the key expansion Mechanisms of the AES Ciphers: Rijndael, Serpent".</li> <li>5. P. Chdowiec, K. Gaj, "Very compact FPGA implementation of the AES algorithm", Cryptographic hardware and embedded systems (CHES 2003), LNCS vol. 2779, pp. 319-333, Springer-Verlog, October 2003.</li> <li>6. G. Rouvroy, F.X. Standaert, J.J. Quisquater, J.D. Legat, , Compact and efficient encryption/decryption module for FPGA implementation of the AES Rijndael very well suited for small embedded applications, Proceedings of the international conference on Information Technology: coding and computing 2004 (ITCC 2004), pp. 583-587, vol. 2, April 2004.</li> <li>7. Tim Good and Mohammed Benaissa, "AES on FPGA from the Fastest to the Smallest", CHES 2005, LNCS 3659, pp. 427-440, 2005. Springer-Verlog Berlin Heidelberg 2005.</li> <li>8. Y. Talwar, C.E. Veni Madhavan, Navin Rajpal, "On Partial Linearization of Byte Substitution Transformation of Rijndael-The AES". Journal of Computer Science 2(1): 48-52, 2006, ISSN1549-3636 © 2006 Science Publications.</li> <li>9. Swinder Kaur and Prof. Renu Vig, "Efficient Implementation of AES Algorithm in FPGA Devices". International Conference on Computational intelligence and Multimedia Applications 2007, DOI 10.1109/ICCIMA -2007.250, pages 179-187,0-7695-3050-8/07, IEEE-(2007) Volume2, pp 179-187.</li> <li>10. J. Elbirt, W. Yip, B. Chatwynd and C. Paes, "An FPGA Implementation and performance Evaluation of the AES block cipher candidate algorithm analyst", Presented at Proc.3rd AES Conf. (AES). Available: <a href="http://csrc.nist.gov/encryption/AES/round2/conf3/aes3paper.html">http://csrc.nist.gov/encryption/AES/round2/conf3/aes3paper.html</a>.</li> <li>11. Thulasimani L. and Madheswarn, "A Single Chip Design and Implementation of AES-128/192/256 Encryption Algorithms", International journal of Engineering Science and Technology (IJEST); ISSN: 0975-5462, Vol.2(5), 2010, 1052-1059.</li> <li>12. M. McLoone and J. V. McCanny, "Rijndael FPGA implementation utilizing look-up tables" , in IEEE Workshop on Signal processing systems, Sept. 2001, pp. 349-360.</li> <li>13. Amandeep Kaur, Puneet Bhardwaj and Naveen Kumar, "FPGA Implementation of Efficient Hardware for the Advanced Encryption Standard", in IJITEE; ISSN: 2278-3075, Volume-2, Issue-3, February 2013.</li> </ol>		1-8
2.	<b>Authors:</b>	<b>A.E.Al-Salami</b>	
	<b>Paper Title:</b>	<b>Quantifying Physical Parameters of Quaternary Glass with Composition Tellurium- Niobium - Zinc-Lithium Oxide</b>	
	<p><b>Abstract:</b> In the present work the tellurite glass a function of alkali metals as network intermediate with composition 85TeO<sub>2</sub>/ 5Nb<sub>2</sub>O<sub>5</sub>/ 5ZnO/ 5Li<sub>2</sub>O in mol% were been prepared. These glasses contain Li<sub>2</sub>O can be promising used in optical devices because it has advantage optical properties compared with other tellurite glasses. The physical parameters of these glass studied with respect to values of density, <math>\rho</math>, molar volume, <math>V_m</math>, linear refractive indices, <math>n_0</math>, nonlinear refractive index, <math>n_2</math>, electronic polarizability, <math>\alpha_m</math>, molar refraction, <math>R_m</math>, metallization criterion, <math>M(n_0)</math>, and third order non-linear optical susceptibility, <math>\chi(3)</math>. The Sellmeier gap energy, <math>E_s</math>, and dispersion energy, <math>E_d</math>, were calculated besides the theoretical third order of nonlinear optical susceptibility, <math>\chi(3)</math>, calculated by used Lines model. Measure the thermal stability and the glass transition temperature by differential thermal analysis (Shimadzu DTA 50) which can be determine the glass transition temperature, <math>T_g</math>, onset crystallization temperature, <math>T_c</math>, and melting temperature, <math>T_m</math>. These glass have values as; <math>\rho = 5.231 \text{ gm/cm}^3</math>, <math>V_m = 28.75 \text{ cm}^3</math>, <math>n_0 = 2.2228</math> at 435.84 nm, <math>\alpha_m = 6.478 \text{ Å}^3</math> and <math>R_m = 16.024 \text{ mol-l/cm}^3</math> at 435.84nm. and <math>E_s = 6.54 \text{ eV}</math>, <math>E_d = 20.65 \text{ eV}</math>.</p> <p><b>Keywords:</b> Oxide glass; linear refractive index; dispersion energy.</p> <p><b>References:</b></p> <ol style="list-style-type: none"> <li>1. A. Assadi, K. Damak, R. Lachheb, A. Herrmann, E. Yousef, C. Russel, R. Maalej "SPECTROSCOPIC AND LUMINESCENCE CHARACTERISTICS OF ERBIUM DOPED TNZL GLASS FOR LASING MATERIALS" ,J. Alloys&amp; Compounds 620 (2015), 129–136.</li> <li>2. E Yousef, A E Al-Salami, A Salem, and E R, Shaaban" Optical and kinetics studies of titanium- zinc-niobium-tellurim oxides glass" Phys. Scr. 83 (2011) 01570</li> <li>3. El Sayed Yousef, A. E. Al-salami, E. R. Shaaban "A TEM study and non-isothermal crystallization kinetic of tellurite glass-ceramics," J. Material Science 45 (2010) 5929–5936.</li> <li>4. El Sayed Yousef, Mario Horzel, Christian Russel "The effect of CdS addition on linear and non-linear refractive indices of glasses in the system TeO<sub>2</sub>/Nb<sub>2</sub>O<sub>5</sub>/ZnO," J. Non-Cryst. Solids 354 (2008) 4675.</li> <li>5. H. Takebe, S. Fujino, K. Morinaga "REFRACTIVE-INDEX DISPERSION OF TELLURITE GLASSES IN THE REGION FROM 0.40 TO 1.71 MM," J. Am. Ceram. Soc., 77[9] (1994) pp. 2455–2457.</li> <li>6. M. Didomenico, S. H. Wemple "Oxygen-Octahedra Ferroelectrics. I. Theory of Electro-optical and Nonlinear optical Effects," J. Appl. Phys. 40 [2] 720 (1969) pp.720-734.</li> <li>7. El Sayed Yousef, A E Al-Salami, Mario Hotzel "Optical and thermal characteristics of glasses based on TeO<sub>2</sub>," Bull. Mater. Sci., Vol. 35, No. 6, (2012) pp.961-967.</li> </ol>		9-13

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<b>Paper Title:</b>	<b>Superiority of Agile over Software Models</b>
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	<p><b>Abstract:</b> The Software Development Life Cycle (SDLC) is a structure imposed on the development of software product and also known as Software Development Processes. It is often considered as a subset of a system development life cycle. There are numerous SDLC models widely used for developing software. The SDLC model gives a theoretical guide line regarding development of the software. The every SDLC has its own benefits and drawbacks according to that we decide which models should be implemented under which conditions. The concept of system lifecycle models came into existence that emphasized on the need to follow some structured approach towards building new or improved system. In this paper, the comparative study made on standard life cycle models namely rapid application development, Agile Development Model, V-shaped model, spiral model, incremental model and waterfall model, prototype.</p>	
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	<p><b>Keywords:</b> Software Development, Life Cycle, Model, Agile Development, Extreme Programming, Dynamic, Adaptive.</p>	
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	<p><b>Paper Title:</b> Decentralized Control Based On Active Disturbance Rejection Controller: An Application Quadruple Tank System</p>	
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	<p><b>Abstract:</b> This paper presents Decentralized Control for the Quadruple Tank System (QTS) Based on Active Disturbance Rejection Controller (ADRC). The most remarkable advantages of the proposed approach are that it is not depend on the accuracy of mathematical model of the plant along with its simple structure and the ability of strong disturbance rejection. Often, there are some difficulties in designing of suitable controller for Quadruple Tank System due to the interaction between the inputs and outputs of this system. With the proposed control method, the cross coupling within the system treated as a disturbance, which is estimated using extended state observer (ESO) and then actively rejected. The Quadruple Tank System is represented using set of nonlinear differential equations</p>	<b>20-25</b>
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and the ADRC controller is applied directly on the nonlinear model of the system. The effectiveness of the proposed approach is validated via simulation results obtained under MATLAB environment. The results show that the ADRC gives better results compared to nonlinear optimal control strategy.

**Keywords:** Active Disturbance Rejection Controller Decentralized Control, Quadruple Tank System, The Nonlinear Coupling.

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